

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,959,271 B1  
DATED : October 25, 2005  
INVENTOR(S) : Peter Ballam

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [74], should read:

-- [74] *Attorney, Agent, or Firm* --Lisa K. Jorgenson; James H. Morris; Wolf, Greenfield & Sacks, P.C. --.

Column 1,

Line 50, should read:

-- behaviour of circuitry described using a hardware descrip- --.

Line 60, should read:

-- as VHDL. --.

Column 4,

Line 24, should read:

-- particular 'X' on an output can indicate the an error has --.

Column 5,

Line 21, should read:

-- 4. .ends --.

Column 6,

Line 11, should read:

-- in one of the many supported simulators. Additional param- --.

Line 13, should read:

-- These are then used to alter the behaviour of the tool and the --.

Column 7,

Line 12, should read:

-- midband voltage using a weaker strength drive than --.

Lines 23 and 24, should read:

-- carried out using the expanded logic package and the results are verified against the expected reference results from the VHDL --.

Line 27, should read:

-- find potential bad transitions. --.

Column 9,

Line 46, should read:

-- In the following, R1 can have a resistance of 100 Ohms, R2 --.

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,959,271 B1  
DATED : October 25, 2005  
INVENTOR(S) : Peter Ballam

Page 2 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11,

Line 24, should read:

-- voltages. The stimulus for these are made by generating --.

Line 24, should read:

-- /name/A:I STD\_logic --.

Column 15,

Line 22, should read:

-- Ons 0 0 --.

Column 16,

Line 14, should read:

-- original database. In steps S9, S10 and S11, the Xs are --.

Column 17,

Line 39, should read:

-- and S14 --.

Column 18,

Line 23, should read:

-- WP-W-PROGRESS, Percentage of vectors parsed=100% - completed --.

Column 19,

Lines 34-35, should read:

-- results of the model with results from running an analog model of the circuit, identifying whether the digital model is --.

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,959,271 B1  
DATED : October 25, 2005  
INVENTOR(S) : Peter Ballam

Page 3 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 20,

Lines 23-24, should read:

-- said model;

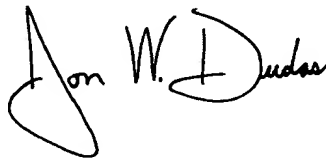
means for re-running the model by applying said expanded set of signals to the model to obtain a --.

Lines 27, should read:

-- the second set of results and providing an output --.

Signed and Sealed this

Seventh Day of February, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" and "Dudas" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*